

#### ABSTRACT OF THE DISCLOSURE

A system for operating in parallel plural non-break power units (1; 2) each including an inverter (15; 25) inserted between an input power source and a parallel bus and an AC switch (17; 27) inserted between the bypass power source and the parallel bus and having an inverter power supply mode via inverter (15; 25) and a bypass source power supply mode via AC switch (17; 27) when the inverter stops. Each unit includes a switch driver (1001; 2001) for generating an AC switch driving signal in response to an inverter power supply signal based on the output signal of a sequence control circuit (1002; 2002). Inverter mode signal output terminals are mutually connected. The switch driver (1001; 2001) generates the relevant AC switch driving signal on the basis of inverter supply signal synthesized on a per-unit basis.